

- 1 => GND => GND
- 2 => IR => RECV\_PIN
- 3 => SER (14) => dataPin
- 4 => SCK (11) => clockPin
- 5 => SWITCH 0
- 6 => RCK (12) => latchPin
- 8 => ~G (13) => enablePin
- 9 => Vcc => +5V

Q0	Q1	Q12	Q13	Q14	Q15	0
0	0	X	X	X	X	0
1	1	1	1	1	1	1
1	1	0	0	0	0	SW
1	0	1	1	1	1	SW1
1	0	1	1	0	1	SW2
1	0	1	0	1	1	SW3
1	0	0	1	1	1	SW4
0	1	1	1	1	0	SW5
0	1	1	1	0	1	SW6
0	1	1	0	1	1	SW7
0	1	0	1	1	1	SW8

